

PATENT ABSTRACTS OF JAPAN

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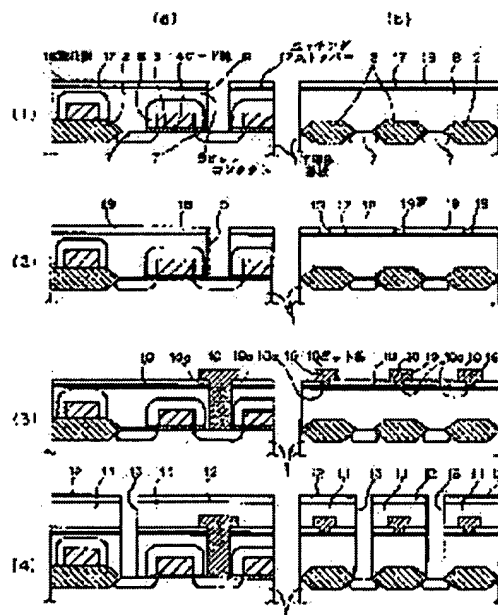
KOBAYASHI YASUTAKA

(54) SEMICONDUCTOR MEMORY AND ITS MANUFACTURE

(57)Abstract:

PROBLEM TO BE SOLVED: To eliminate the need for apprehension about the movement of bit lines during a heat treatment.

SOLUTION: When a semiconductor memory is provided with bit lines 10 for inputting and outputting data to and from memory cells formed on the surface of a semiconductor substrate 1, grooves 19 which are extended along the wiring direction of the bit lines 10 are formed into an oxide film 18 and the bit lines 10 are arranged in such a state that the bit lines 10 are engaged with the grooves 19. Since the bit lines 10 are engaged with the grooves 19, the bit lines 10 are firmly fixed to the oxide film 18. Therefore, the bit lines 10 are not moved even when stresses are applied to the side faces of the bit lines 10.



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